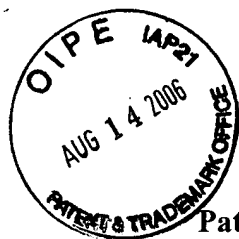


3FW

Fang 2-4



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Fang et al.  
Case: 2-4  
Serial No.: 10/776,701  
Filing Date: February 11, 2004  
Group: 2627  
Examiner: Daniel L. Negron

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: [Signature] Date: August 10, 2006

Title: Impedance-Matched Write Circuit with Shunted Matching Resistor

TRANSMITTAL LETTER

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Response to Office Action relating to the above-identified patent application. There is no additional claim fee due in conjunction with the Amendment.

In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

[Signature]

Date: August 10, 2006

Kevin M. Mason  
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(203) 255-6560



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15

RESPONSE TO OFFICE ACTION

20 Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

25 Sir:

In response to the outstanding Office Action, dated May 10, 2006, Applicants submit the following remarks.